

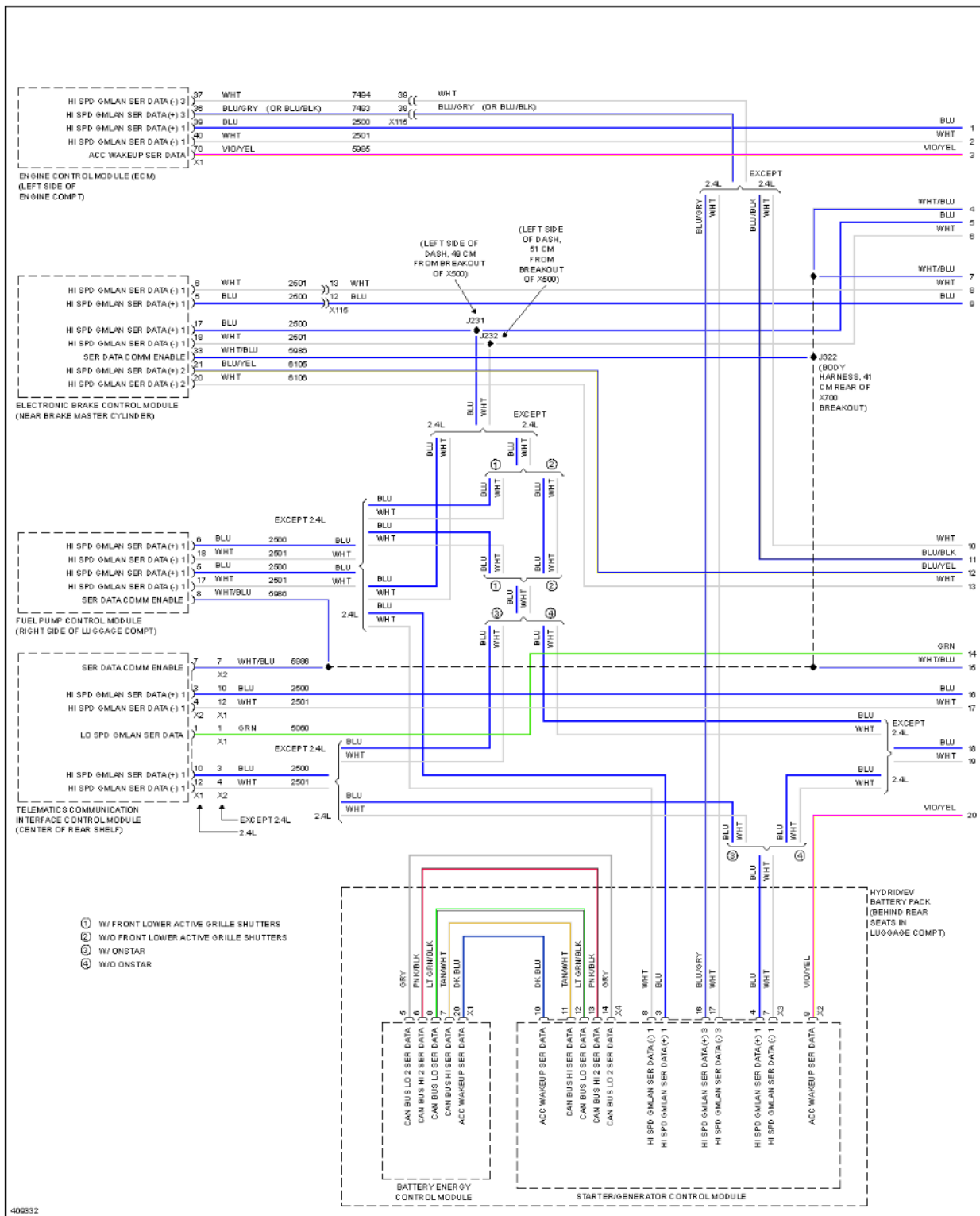


Fig. 20: Computer Data Lines Circuit (1 of 4)

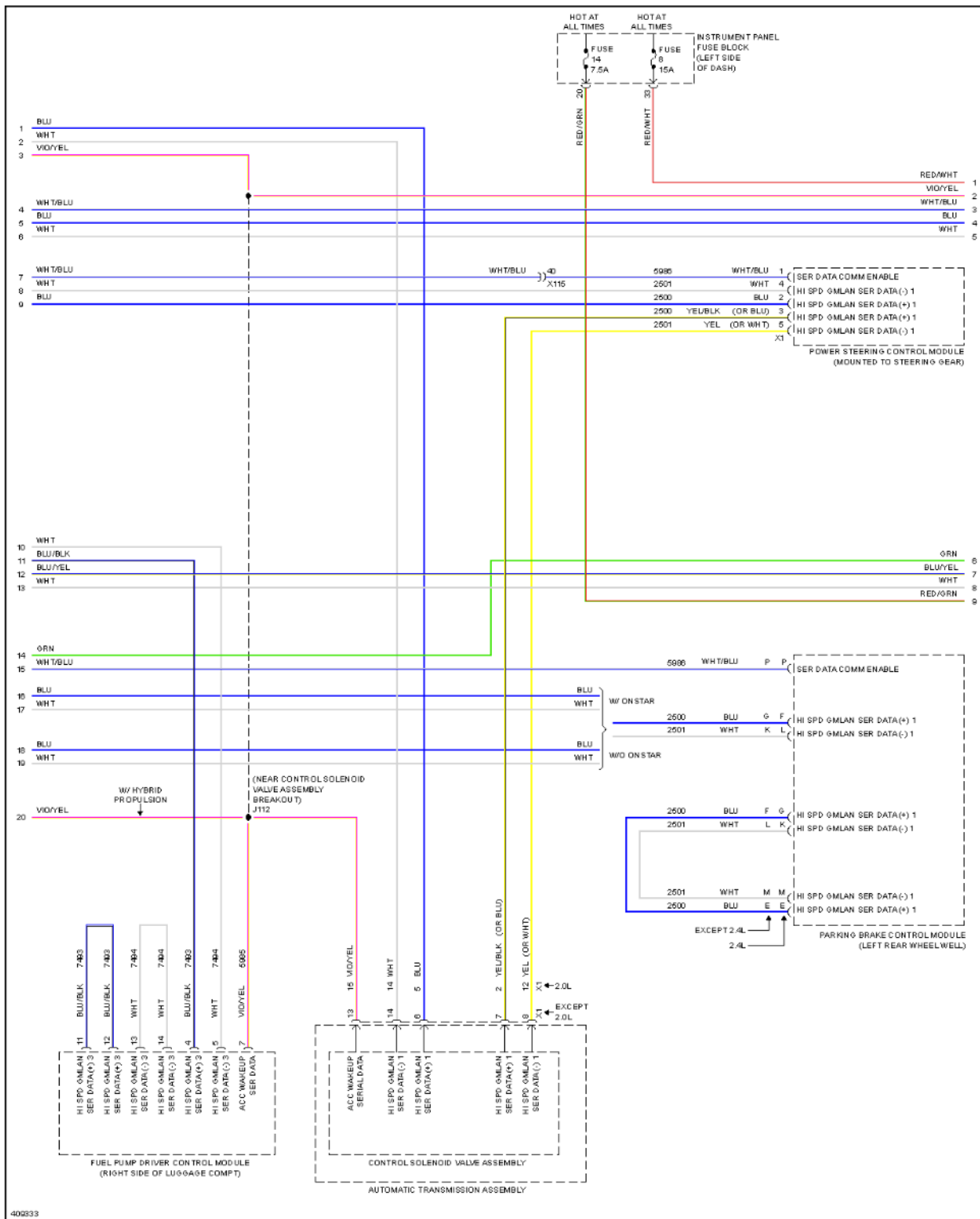


Fig. 21: Computer Data Lines Circuit (2 of 4)

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