

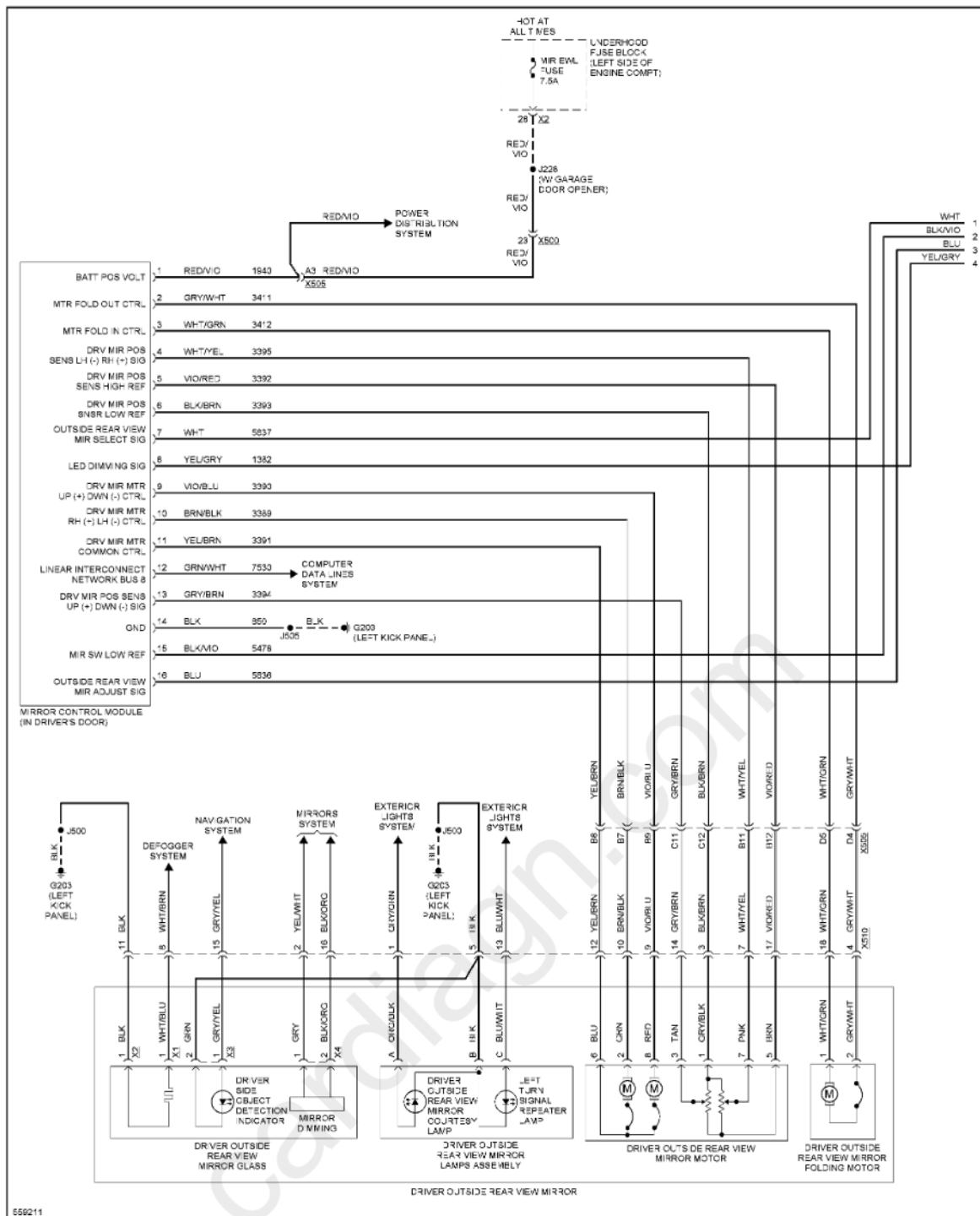


Fig. 72: Memory Mirrors Circuit (1 of 2)

