

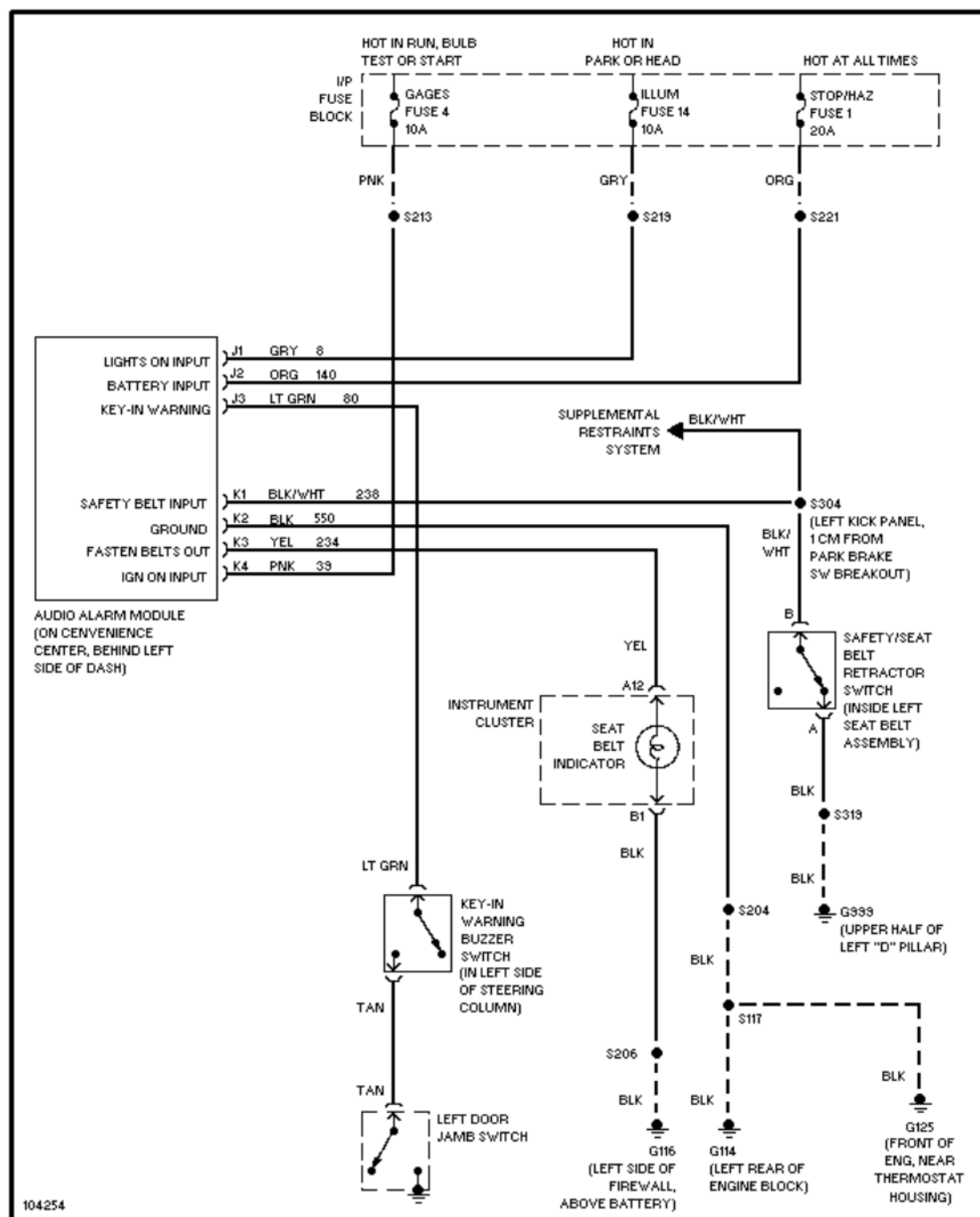


Fig. 43: Warning System Circuits