

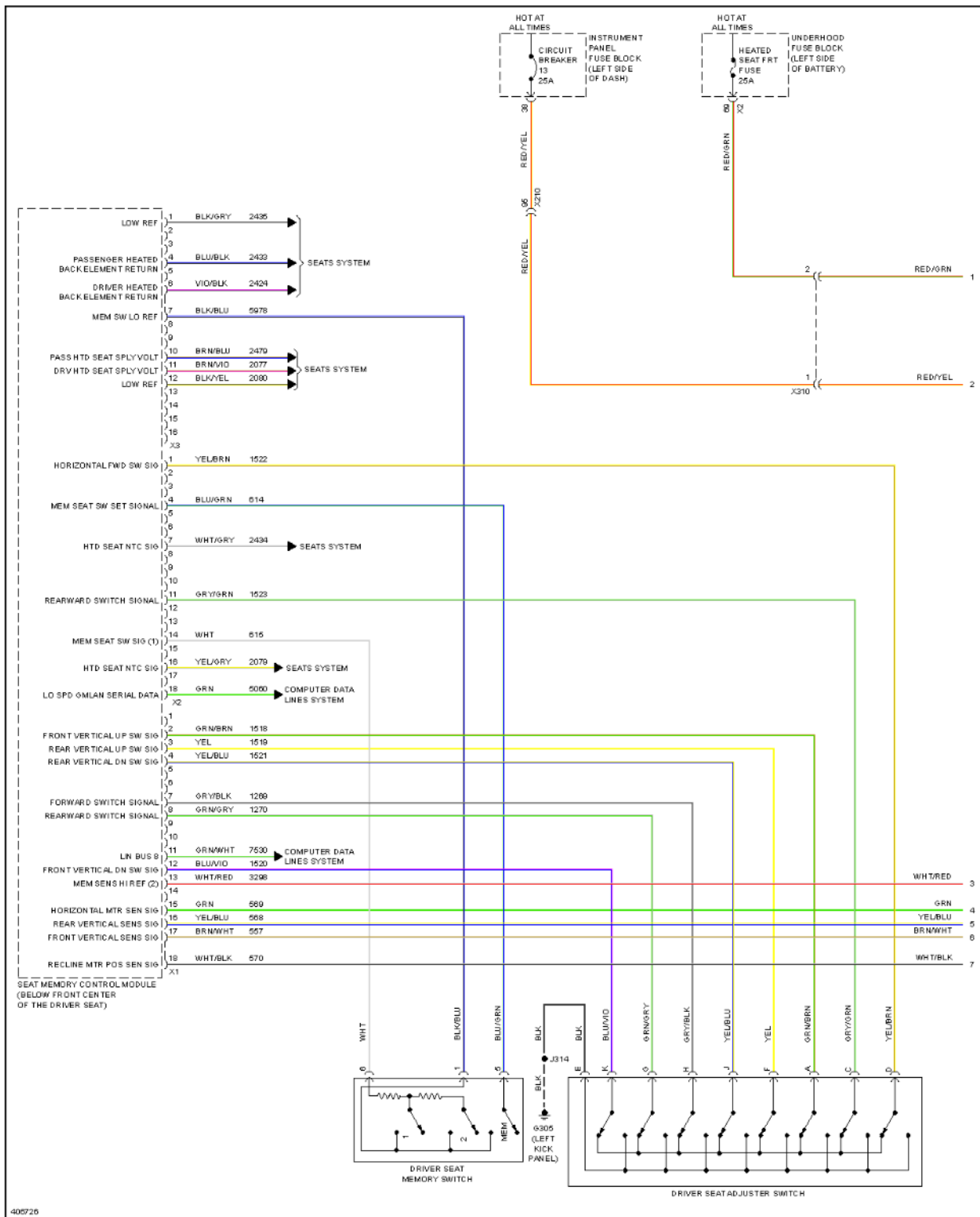


Fig. 75: Memory Seat Circuit (1 of 2)

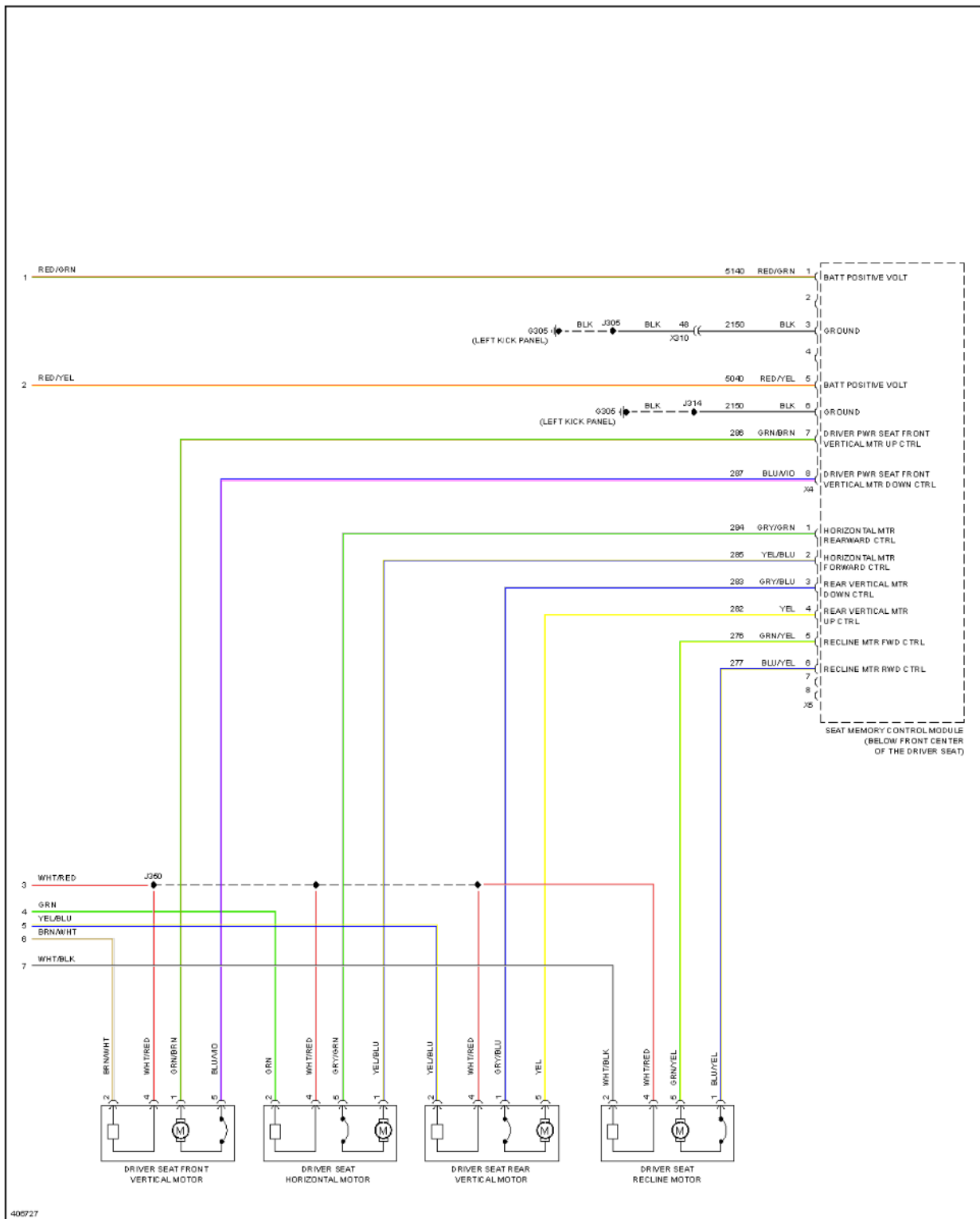


Fig. 76: Memory Seat Circuit (2 of 2)

NAVIGATION

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