

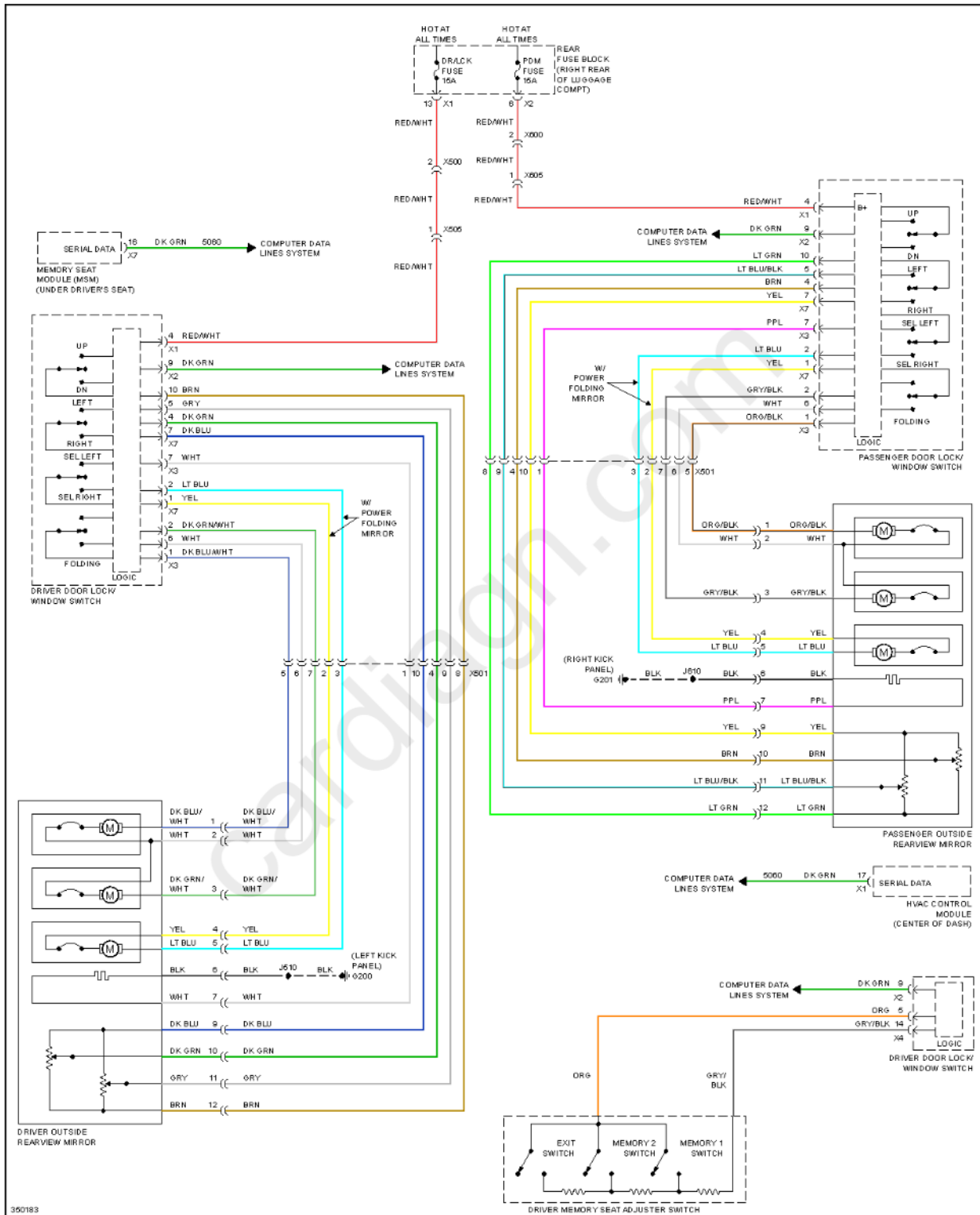


Fig. 59: Memory Mirrors Circuit