

Fig 1: Supplemental Restraints Circuit (1 of 3)



Fig 2: Supplemental Restraints Circuit (2 of 3)

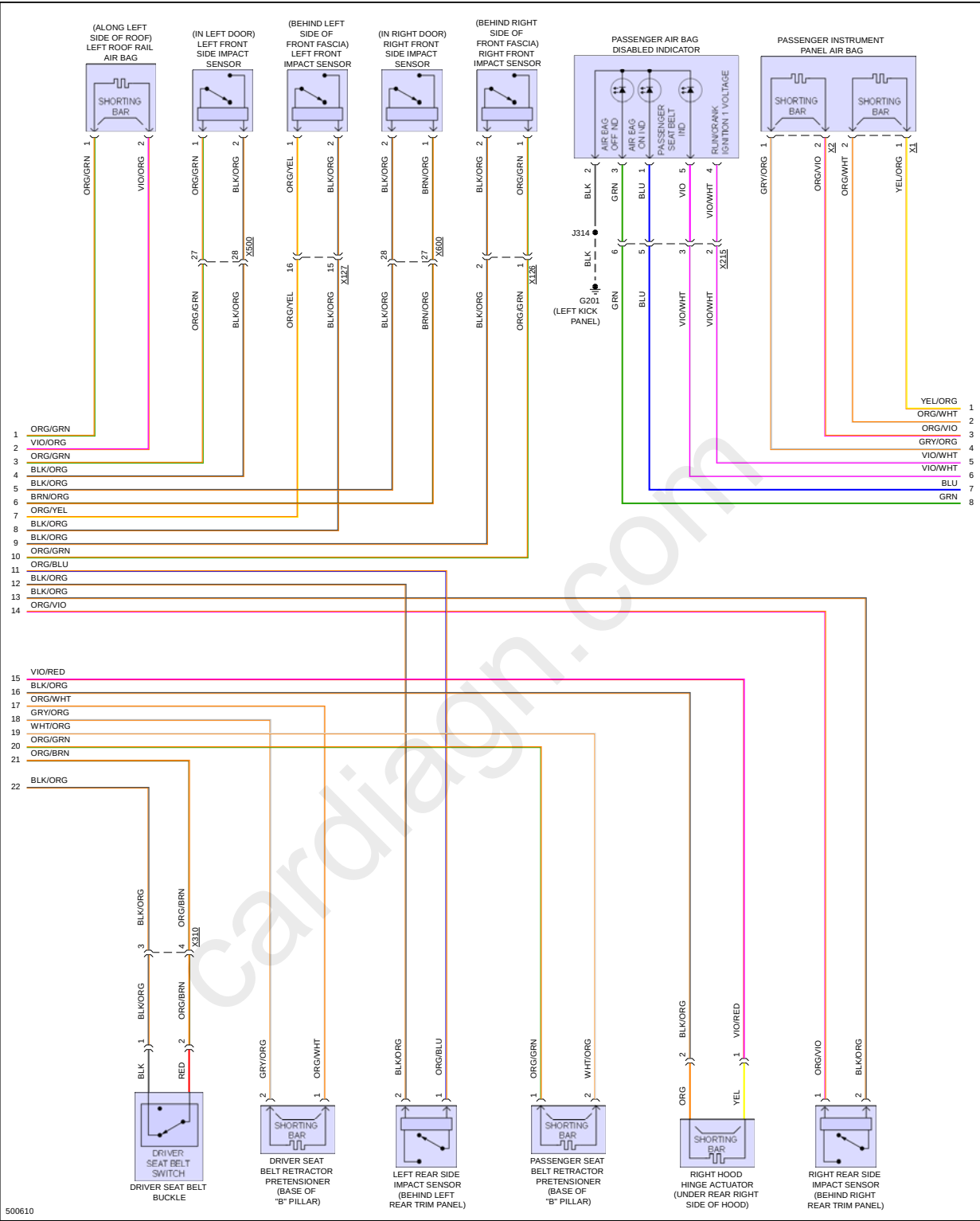


Fig 3: Supplemental Restraints Circuit (3 of 3)

