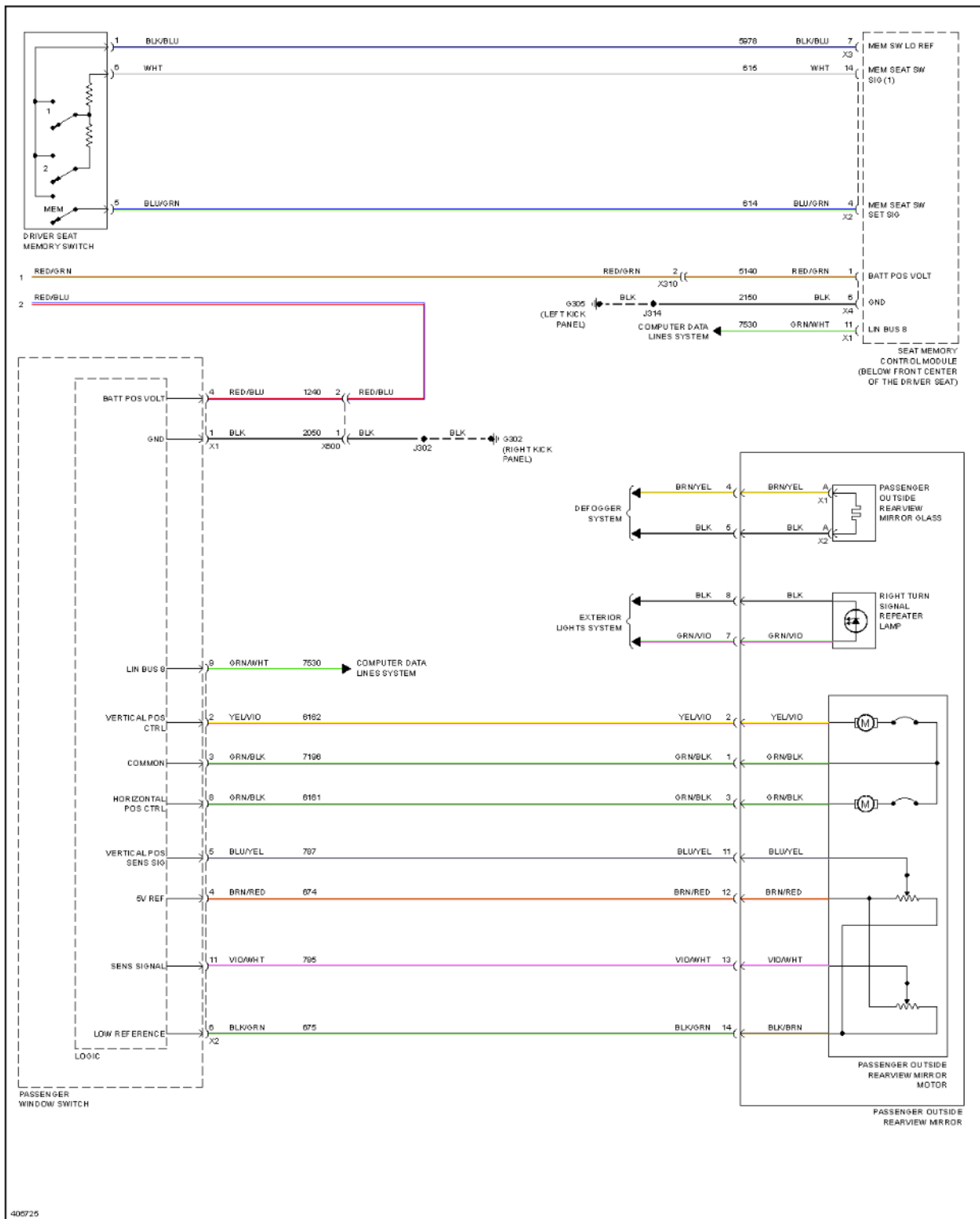


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**Fig. 74: Memory Mirrors Circuit (2 of 2)**